

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT****Complete if Known**

Application No.	10/773,385
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First Named Inventor	Donald S. Steiss
Art Unit	2183
Examiner Name	Eric Coleman
Attorney Docket Number	22347-08564

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**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document No. Number - Kind Code <sup>2</sup> (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
EC.	A1	US-2002/0062435 A1	05-23-2002	Nemirovsky et al.
EC.	A2	US-2001/0032307 A1	10-18-2001	Rohlman et al.

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup>

**OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>6</sup>
EC.	C1	REGNIER, G., et al., "ETA: Experience with an Intel® Xeon™ Processor as a Packet Processing Engine," IEEE Proceedings of the 11 <sup>th</sup> Symposium on High Performance Interconnects, August 20-22, 2003, pp. 76-82.	

Examiner Signature	<i>Eric Coleman</i>	Date Considered	11/2/07
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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